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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		<b>First Inventor</b>	Ware, Frederick A.
Page 1 of 1		<b>Art Unit</b>	2187
		<b>Examiner</b>	Nguyen, Than Vinh
		<b>Atty. Docket No.</b>	RBS2.P049

<b>U.S. Patent Documents</b>					
<b>Examiner Initials*</b>	<b>US Patent Document</b>		<b>Name of Patentee or Applicant of Cited Document</b>	<b>Date of Publication</b>	<b>Relevant Pages, Columns, Lines</b>
	<b>Number</b>	<b>Kind</b>			
	6,232,792		Starr, Jonathan E	05/15/2001	

<b>Non Patent Literature Documents</b>		
<b>Examiner Initials</b>	<b>Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published</b>	<b>Trans-lation</b>
	"Standard for a High-Speed Memory Interface (SyncLink)," IEEE Draft 0.99 IEEE P 1596.7 199X, 1996, 66 pages.	
	Rambus Inc. Data Sheet, "8/9-Mbit (1M x 8/9) and 16/18-Mbit (2M x 8/9) RDRAM", March 1996, pages 1-30 (Dup of 3809)	

<b>Examiner Signature</b>		<b>Date Considered</b>
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.